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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/761,310	01/22/2004	Yasunaga Iseda	61282-057	3085
7:	590 12/09/2004		EXAMINER	
McDERMOTT, WILL & EMERY			HO, TU TU V	
600 13th Street, N.W. Washington, DC 20005-3096			ART UNIT	PAPER NUMBER
washington, 2	20003 3030		2818	

DATE MAILED: 12/09/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)			
Office Action Summary		10/761,310	ISEDA ET AL.			
		Examiner	Art Unit			
		Tu-Tu Ho	2818			
Period fo	The MAILING DATE of this communication app or Reply	ears on the cover sheet with the c	orrespondence addres	SS		
THE - Exte after - If the - If NC - Failt Any	ORTENED STATUTORY PERIOD FOR REPLY MAILING DATE OF THIS COMMUNICATION. Insions of time may be available under the provisions of 37 CFR 1.13 SIX (6) MONTHS from the mailing date of this communication. In period for reply specified above is less than thirty (30) days, a reply operiod for reply is specified above, the maximum statutory period varieto reply within the set or extended period for reply will, by statute reply received by the Office later than three months after the mailing led patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be tir within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed /s will be considered timely. In the mailing date of this communicity (35 U.S.C. § 133).	unication.		
Status						
1)🖂	Responsive to communication(s) filed on 22 Ja	anuary 2004.				
2a)[_		n is <b>FINAL</b> . 2b)⊠ This action is non-final.				
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
	closed in accordance with the practice under E	Ex parte Quayle, 1935 C.D. 11, 4	53 O.G. 213.			
Disposit	ion of Claims					
4)⊠	Claim(s) 1-17 is/are pending in the application.					
	4a) Of the above claim(s) is/are withdrawn from consideration.					
5)□	Claim(s) is/are allowed.					
6)□	Claim(s) is/are rejected.					
7)	Claim(s) is/are objected to.					
8)⊠	Claim(s) <u>1-17</u> are subject to restriction and/or election requirement.					
Applicat	ion Papers					
9) The specification is objected to by the Examiner.						
10)	10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.					
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).					
_	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).					
11)	The oath or declaration is objected to by the Ex	caminer. Note the attached Office	Action or form PTO-	152.		
Priority	under 35 U.S.C. § 119		·			
,	Acknowledgment is made of a claim for foreign  All b) Some * c) None of:  Certified copies of the priority document	s have been received.	, , , , , ,			
	<ul> <li>2. Certified copies of the priority documents have been received in Application No</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage</li> </ul>					
	3. Copies of the certified copies of the prio application from the International Bureau		ed in this National Sta	ıge		
* See the attached detailed Office action for a list of the certified copies not received.						
Attachmer						
Notice of References Cited (PTO-892)     Notice of Draftsperson's Patent Drawing Review (PTO-948)		4) L Interview Summary Paper No(s)/Mail D				
3) Infor	rmation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) er No(s)/Mail Date		Patent Application (PTO-15	2)		

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## **DETAILED ACTION**

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## Election/ Restriction

Claims 1-17 are pending in this application.

- 1. Restriction to one of the following inventions is required under 35 U.S.C. 121:
  - I. Claims 1-5 and 9-17, drawn to a semiconductor device having a bump and a rewiring layer and a method of forming thereof, classified in class 257, subclass 737.
  - **II.** Claims 6-8, drawn to a method of forming a semiconductor device having a bump and a rewiring layer, classified in class 438, subclass 613.
- 2. The inventions are distinct, each from the other because of the following reasons: Inventions II and I are related as process of making and product made. The inventions are distinct if either or both of the following can be shown: (1) that the process as claimed can be used to make other and materially different product or (2) that the product as claimed can be made by another and materially different process (MPEP § 806.05(f)). In the instant case unpatentability of Invention I would not necessarily imply unpatentability of Invention III, since the device of Invention I could be made by processes materially different from those of Invention II. For example, the bump of Invention I could be formed just by simply arranging said bump; i.e., without the required step detecting whether or not an edge of the uppermost layer wiring exists within a region where the bump is to be arranged of Invention II.

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3. Because these inventions are distinct for the reasons given above and have acquired a separate status in the art because of their different classification and their recognized divergent subject matter, restriction for examination purposes as indicated is proper.

- 4. Applicant is advised that the response to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).
- 5. Applicant is reminded that upon the cancellation of claims to a non-elected invention, the inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more of the currently named inventors is no longer an inventor of at least one claim remaining in the application. Any amendment of inventorship must be accompanied by a diligently filed petition under 37 CFR 1.48(b) and by the fee required under 37 CFR 1.17(h).
- 6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tu-Tu Ho whose telephone number is (571) 272-1778. The examiner can normally be reached on 6:30 am 5:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, DAVID NELMS can be reached on (571) 272-1787. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

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may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Tu-Tu Ho

December 06, 2004